

## IN THE CLAIMS

Claims 1-6 are cancelled.

7. (Currently amended) A GPU interface device, comprising:

a substrate for coupling a GPU to a PCB (printed circuit board), the substrate having a first interface for a connection to the GPU and a second interface for a connection to the PCB, the first interface having a customizable attachment footprint for implementing a connection to differing GPU types while maintaining the second interface for the connection to the PCB; ~~and~~

a video BIOS component for providing video BIOS functions, wherein the video BIOS chip is programmed to support a version of the GPU; and

a plurality of circuits included in the substrate for coupling the video BIOS component with the first interface for the differing GPU types.

8. (Currently amended) The GPU interface device of claim 7 wherein the customizable attachment footprint of the first interface includes a plurality of ground balls configured to support high-speed signaling of a the GPU.

9. (Original) The GPU interface device of claim 7 wherein the second interface is configured to support at least two DACs for a plurality of displays.

10. (Original) The GPU interface device of claim 7 wherein the second interface is configured to support a digital video output for a digital display.

11. (Original) The GPU interface device of claim 7 wherein the differing GPU types are identical GPU integrated circuits and differing amounts of memory.

12. (Canceled).

Claims 13-18 are cancelled.

19. (New) A device for interfacing a GPU (graphics processor unit) to a PCB (printed circuit board), comprising:

a substrate having a first interface for a connection to the GPU and a second interface for a connection to the PCB, the first interface customizable to implement a connection to differing GPU types while maintaining the second interface for the connection to the PCB;

a video BIOS component for providing video BIOS functions; and

a plurality of circuits included in the substrate for coupling the video BIOS component with the first interface for the differing GPU types.

20. (New) The GPU interface device of claim 19 wherein the customizable attachment footprint of the first interface includes a plurality of ground balls configured to support high-speed signaling of the GPU.

21. (New) The GPU interface device of claim 19 wherein the second interface is configured to support at least two DACs (digital to analog converters) for a plurality of displays.

22. (New) The GPU interface device of claim 19 wherein the second interface is configured to support a digital video output for a digital display.

23. (New) The GPU interface device of claim 19 wherein the differing GPU types are identical GPU integrated circuits and differing amounts of memory.

24. (New) A substrate device for interfacing a processor to a PCB (printed circuit board), comprising:

a substrate having a first interface for a connection to the processor and a second interface for a connection to the PCB, the first interface customizable to connect differing processor types while maintaining the second interface for the connection to the PCB;

a video BIOS integrated circuit die for providing video BIOS functions; and

a plurality of circuits included in the substrate for coupling the video BIOS integrated circuit die with the first interface for the differing processor types.

25. (New) The processor interface device of claim 24 wherein the customizable attachment footprint of the first interface includes a plurality of ground balls configured to support high-speed signaling of the processor.

26. (New) The processor interface device of claim 24 wherein the second interface is configured to support at least two DACs (digital to analog converters) for a plurality of displays.

27. (New) The processor interface device of claim 24 wherein the second interface is configured to support a digital video output for a digital display.

28. (New) The processor interface device of claim 24 wherein the differing processor types are identical processor integrated circuits and differing amounts of memory.